



2661

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:
Asher Hazanchuk

Application No.: 09/891,710

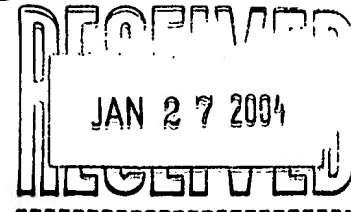
Filed: June 26, 2001

Title: Parallel Samples, Parallel Coefficients,
Time Division Multiplexing Correlator
Architecture

Attorney Docket No. ALT.P001

Examiner: Not Yet Assigned

Art Unit: 2661



Assistant Commissioner for Patents
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

This Information Disclosure Statement is believed to be filed before the mailing date of a first Office Action on the merits under 37 CFR §1.97(b). Accordingly, it is believed that no fees are due in connection with the filing of this Information Disclosure Statement. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 50-1624.

The reference listed in the attached PTO Form PTO/SB/08A, a copy of which is attached, may be material to examination of the above-identified patent application. Applicant submits these references in compliance with his duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these references of official record in this application.

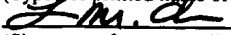
CERTIFICATE OF MAILING

Date of Deposit January 15, 2004

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to the Assistant Commissioner for Patents, Alexandria, VA 22313-1450 on the date indicated above.

Lawrence M. Cho

(Typed or printed name of person mailing correspondence)


(Signature of person mailing correspondence)

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

If there are any additional charges, please charge them to Deposit Account No. 50-1624.

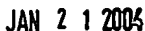
Respectfully submitted,

Dated: January 15, 2004



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PTO/SB/08A (08-03)

Approved for use through 07/31/2006. OMB 0651-0031

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Complete if Known

Application Number	09/891,710
Filing Date	June 26, 2001
First Named Inventor	Asher Hazanchuk
Art Unit	2661
Examiner Name	Not Yet Assigned
Attorney Docket Number	ALT.P001

Sheet	1	of	2
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U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

Examiner Signature		Date Considered	
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 2

of

2

Complete if Known

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First Named Inventor	Asher Hazanchuk
Art Unit	2661
Examiner Name	Not Yet Assigned
Attorney Docket Number	ALT.P001

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Technology Center 2600**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		KEN CHAPMAN, PAUL HARDY, ANDY MILLER, and MARIA GEORGE, CDMA Matched Filter Implementation in Virtex Devices, XILINX XAPP212 (v1.0), March 31, 2000	

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